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Full Color Display Structures using

Pseudomorphic Cladded Quantum Dot Nanophosphor Thin Films

BACKGROUND OF THE INVENTION

(1) Field of the Invention: This invention relates to color display devices, in particular to pseudomorphic cladded quantum dot nanophosphor thin film color display devices.

(2) Description of the Related Art: Cladded quantum dots have been extensively reported in the literature as a way of improving light emission intensity in quantum dots.[Ref.1,2] For example, cladded nanocrystals (CNCs) having CdSe core and ZnS cladding (or shell) have been used to produce high photoluminescence efficiency quantum dots.[Ref.3,4] These dots however, suffer from appreciable lattice mismatch between the core and cladding layer(s) that in turn contributes to a number of undesirable features such as:

- i) Introduction of interfacial states responsible for photoluminescence (PL) and electroluminescence (EL) quenching.
- ii) Limitation in growing thick cladding layers, as a result of lattice mismatch which becomes more pronounced in luminescence quenching as the cladding thickness increases.[Ref.4]
- iii) The blinking behavior of these CNCs, closely related to exciton confinement in these nanoparticles, is greatly affected by defects such as that introduced by the lattice mismatch between core and cladding layer(s).[Ref.5]

Quantum dot devices require incorporation of dots in thin film forms that can be used for electronic excitation. The size distribution, composition variation and non- periodic placement of these quantum dots greatly affect the emitted wavelength, and overall device performance, especially for lasing.[Ref.6] Growth of quantum dots by self-organization has been reported to demonstrate lasing.[Ref.7] However, these devices do not yield the expected improvement in the threshold current density predicted for quantum dot lasers.

Liquid phase (solution) grown nanocrystals possess certain limitations in creating core-shell architectures with improved lattice-matching.[Ref.8] Although the lattice mismatch of ZnS cladding is significant to that of the CdSe core, these CNCs emit brighter than their uncladded counterparts.[Ref.2] Cladding with CdS produces less

strained lattices. This in turns makes the CdSe/CdS core/shell CNCs to photoluminescence brighter than the CdSe/ZnS CNCs. Restricting the cladding layer thickness to only few monolayers provides one way to reduce the interfacial defects (via strained layers). Once the thickness of the cladding is increased, the photoluminescence quantum efficiency of these CNCs plummets as a result of strain-induced defects, which become dominant. Therefore, the present state of the art is based on thinly cladded CNC nanocrystals. These CNCs are further coated with surface passivation organic surfactant layers, which in turn play a major role to the photoluminescence efficiency of these nanocrystals. Nanocrystals prepared this way, when incorporated in electroluminescent (EL) devices, utilizing carrier injection invariably yields poor EL efficiencies.

A layer of trioctyl phosphine oxide (TOPO) is presently one of the best surface passivating agent used in the liquid phase growth of these CNCs.[Ref.2,4] TOPO in conjunction with TOP (trioctyl phosphine) passivates both cationic (Cd^{2+} , Zn^{2+} , Te^{2+} , etc.) and anionic (Se^{2-} , S^{2-} , O^{2-} , etc.) surface species respectively. Figure 1(a) shows a typical liquid phase grown cladded nanocrystal with a core (1) and a thin cladding (2). The outer cladding layer (3) comprises of both TOPO and TOP. Figure 1(b) shows a single pixel EL structure utilizing these nanocrystals in the form of a thin layer (7) sandwiched between a hole transporting organic layer (6) and top metal electrode layer (8). The hole-transporting layer 6 is realized on a thin transparent ITO (indium tin oxide) electrode (layer 5), supported on a substrate (4). Poly (p-phenylenevinylene) along with other hole injecting organics has been used with limited success (Ref. 8).

Once the organic passivation agent(s) are partially removed or somehow degrade during device operation, the emission characteristics and brightness of these CNCs are significantly impaired. In addition, the natural inability of the organic phase towards dielectric breakdown poses a significant limitation for these CNCs to find application in field-assisted electroluminescent devices.

An alternate technology is the use of doped nanocrystals, DNCs, [Ref.9] where a dopant is introduced in the quantum dot, such as Mn in ZnS. These DNCs are incorporated in the form of thin films and used for electroluminescence or cathodoluminescence device applications.

Use of cladding layer material that has a higher band gap than the core with similar lattice constant results in a pseudomorphic cladded quantum dot. In this arrangement, the core-cladding lattice mismatch is accommodated as tensile or compressive strain. Having a small mismatch permits the growth of thicker cladding layers as has been shown in pseudomorphic strain layer quantum-wells. This improves the confinement of holes, electrons and excitons inside the core. In addition, these CNCs are less affected by the environment of the outer surface of the cladding layer, thus putting less stringent requirements on additional surface modifying layers. In particular, the known susceptibility of chalcogenides and other semiconducting materials to form oxides in the presence of atmospheric moisture and oxygen justifies the growth of compatible surface passivation layer(s) to further improve their environmental stability. Alternatively, one can use a compatible higher energy gap semiconducting layer before applying the outer passivation layer. Depending on the device structure, the outer layers could be semiconducting or conducting.

Quantum dots have been used to realize nanophosphor for a variety of display, lasers and luminescent-based diagnostic applications. The luminescent properties of these dots however, are strongly influenced by surface states. Cladding layers have been used to reduce these surface states, although the lattice mismatch in the materials for core and cladding creates undesirable quenching defects. In addition, the cladding layer prevents the injection of carriers, with the holes especially, hindered in reaching the core of these nanophosphors.

SUMMARY OF THE INVENTION

An object of this invention is to fabricate a high resolution color display device. Another object of this invention is to fabricate a display device using pseudomorphic cladded quantum dot nanophosphor thin film with high brightness.

This invention describes novel device structures for full color flat panel displays utilizing pseudomorphically cladded quantum dot nanocrystals. In this approach, different colors are obtained by changing the core size and/or composition of the quantum dots while maintaining a nearly defect-free lattice at the core-cladding interface.

Light emission from the quantum dot core is obtained either by injection or by avalanche electroluminescence (EL).

Injection EL involves a novel approach injecting minority carriers in the core from the wide energy gap n and p-layers, through the cladding layer. In the case of inorganic-organic hybrid device structures, hole blocking layer surrounding the CNCs creates a favorable scenario for efficient injection and recombination taking place in the core of the cladded quantum dots. Avalanche EL device structures are proposed using single crystal, polycrystalline and amorphous starting surfaces. Device structures involving epitaxial layers and non-epitaxial growth are used. The ability to grow cladded nanocrystals (CNCs), pseudomorphically on single crystal substrates is also described. These, in conjunction with dielectric layers grown above and below the quantum dot layer(s), are expected to result in low voltage and high brightness CNC avalanche EL devices.

Variations of the disclosed structures are presented for various material systems. These generic EL devices can be addressed using a variety of conventional display drivers, including active and passive matrix configurations. In the case of avalanche/field emission EL, formation and use of nanotip structure is also presented.

BRIEF DESCRIPTION OF THE SEVERAL VIEWS OF THE DRAWINGS

Fig. 1(a) shows the basic structure of conventional cladded nanocrystals, modified at their outer surface with TOPO or TOP.

Fig. 1(a) illustrates a conventional thin-film based EL device.

Fig. 2(a) shows an EL device comprising of pseudomorphic cladded quantum dots sandwiched between thin films of wider energy gap semiconductors.

Fig. 2(b) shows a representative pseudomorphic CNC.

Fig. 3 shows an EL device having multiple layers comprising of pseudomorphic cladded quantum dots sandwiched between epitaxially grown thin films of wider energy gap semiconductors.

Fig. 4 shows a typical white light EL device vertically integrating three primary colors via layers comprising of different pseudomorphic cladded quantum dots whose core size and composition produces the desired color response.

Fig. 5 shows a red, green and blue (RGB) EL display matrix having layers comprising of deferent pseudomorphic cladded quantum dots whose core size and composition produces the desired color response.

Fig. 6 shows an avalanche, non epitaxial device where quantum dots are sandwiched on dielectric layers such as SiON, Ta₂O₅, Ba_xSr_{1-x}TiO₃, PLZT, Zn_xMg_{1-x}S, Zn_xBe_{1-x}S, etc. or their combination. The dielectric layers are grown on ITO, polycrystalline or amorphous silicon with appropriate conductivity, metals, etc.

Fig. 7 shows an injection based EL device incorporating nanophosphors utilizing a variety of amorphous or polycrystalline semiconductors or insulators layers.

Fig. 8(a) shows an injection based hybrid (organic/inorganic) EL device. The quantum dots are incorporated in between the *n*-type (inorganic) and hole-transporting (organic) semiconductor layers.

Fig. 8(b) shows a structure similar to Fig. 8(a) where the organic hole-transporting layer is doped with an oxidative agent.

Fig. 8(c) shows an injection based hybrid (organic/inorganic) EL device. The quantum dots are incorporated within the hole-transporting organic semiconductor layer.

Fig. 8(d) shows a structure similar to Fig. 8(c) where the organic hole-transporting layer is doped with an oxidative agent.

Fig. 9 shows a representative pseudomorphic CNC with an optional outer environmental passivation layer.

Fig. 10(a) shows an all-organic injection based EL device. The CNCs are incorporated within the *p*-type organic semiconductor layer.

Fig. 10(b) shows a structure similar to Fig. 10(a) where the organic *p*-transport medium is doped with an oxidative agent.

Fig. 10(c) CNCs are sandwiched between the hole- and electron-transporting organic semiconductor layer.

Fig. 10(d) shows a structure similar to Fig. 10(c) where the hole-transporting layer is doped with an oxidative agent.

Fig. 11(a) shows an EL structure where a viscous, mellifluous composite (consisting of CNCs, *p*-type organic semiconductors, oxidative agents and viscosity modifying agents having low vapor pressure), is sandwiched between two electrodes.

Fig. 11(b) shows a display based on EL structure of Fig. 11(a) employing a passive-matrix-addressing scheme. Here, rows and columns of the bottom and top electrodes respectively are separated by an elastomeric spacer that form vesicles where the viscous composite can be inject printed with and RGB pattern.

Fig. 12 shows an EL structure employing nanotip Si emitters (realized in the p-Si base layer using the selective area epitaxy through a thin (~10nm) SiO₂ patterned mask having around 0.1 micron spacing between oxide regions) providing minority carriers into the nanophosphor layer. The Si base layer and the nanophosphor layer is reversed biased, channeling electrons and hole in the cores of quantum dots, and subsequently emitting the desired radiation.

DETAILED DESCRIPTION OF THE INVENTION

Figure 2 shows an EL device comprising of pseudomorphic cladded quantum dots sandwiched between epitaxially grown thin films of wider energy gap semiconductors. A silicon/silicon oxide composite layer is realized on sapphire substrate 10, consisting of thin doped Si p/p^+ regions 11, separated by SiO₂ insulating regions 12. The top surface of regions 11 and 12 contain a thin-layer of Si (13) which allows to grow a wider gap semiconductors layer 14, cladded quantum dots 15, and covered with another wider gap semiconductors layer 16. Top contact electrodes 17, running perpendicular to the doped Si regions 11, are formed by metalization. In the case that layer 14 is of p -type semiconductor, layer 15 can be n -type to form to p/n junction that could operate in injection or avalanche mode depending on forward or reverse bias. In the case where layers 11, 13 and 14 are doped n -type, and if layer 16 is thin and lightly n -doped, the metalization layer 17 can form a Schottky barrier. This device can also work in injection or avalanche mode depending on forward or reverse bias. In the case of avalanche operation, the layer forming a reverse barrier junction should be thick enough to produce impact ionization that leads to avalanche multiplication of carriers.

Figure 3 shows a variation of Fig. 2 device having layer 18 consisting of multiple layers of pseudomorphic cladded quantum dots (19), sandwiched between epitaxially grown thin film layers of wider energy gap semiconductors (20). This arrangement forms multiple quantum dot (MQD) structures. Relatively thick MQD structures are suitable

for avalanche type EL devices where as few layers of MQD structures are suitable for injection EL.

Figure 4 shows a variation of Fig. 2 device having the multiple layers comprising of CNCs stacked based on their emission color. For example, depending on the core size and composition, pseudomorphic cladded dots ZnCdSe/ZnSe or ZnCdSe/ZnSSe, emitting red 23, green 22, and blue 21 (RGB) are going to be deposited successively on top of the wider gap semiconductor layer 14, 24 and 25 respectively. If layer 24 and 25 do not include tunnel junctions, this structure would operate in avalanche mode. Insertion of p+/n+ tunnel junctions is required [Ref.10] to run it as an injection device. This structure is geared to produce stable white EL lamps, ensuring that no energy transfer takes place between different color CNCs. These white lamps are expected to retain their chromaticity characteristics during their operating lifecycle.

Ensuring that the spacing between the quantum dots is filled with wider energy gap semiconductor, channeling carriers through the embedded CNCs is expected to be particularly important to lower the driving voltage. These structures are expected to operate at significantly reduced voltages, (in the range from 5-20 V). The enhanced radiative transition rates in pseudomorphic CNCs make them suitable for fast frame rate applications.[Ref.6]

Figure 5 shows a variation of Fig. 2 device structure, where CNCs are stacked based on their emission color to form full color pixelated devices. For example, depending on the core size and composition emitting red 23, green 22, and blue 21 (RGB) are going to be deposited successively to form rows on top of layer 14. This placement of CNC can be accomplished using a variety of deposition techniques. One of these techniques could be the electrostatic deposition of CNCs on the lower bottom electrodes (layer 12) that are appropriately biased.

Figure 6 shows the cross-sectional schematic of an avalanche, non-epitaxial EL device structure showing only one pixel. ITO layer 27 is grown on substrate 26 forming row of electrodes (only one pixel is shown), followed by a dielectric layer 28. The quantum dots (layer 15) are deposited on the said dielectric layer 28. This is followed by the deposition of another dielectric layer 28, and a metal layer 29 forming columns of electrodes (only one electrode is shown) complete a passively addressed display matrix. The dielectric

layers 28 could be of SiON, Ta₂O₅, Ba_xSr_{1-x}TiO₃, PLZT, Zn_xMg_{1-x}S, Zn_xBe_{1-x}S, etc. or their combination. The electrode layer 27 could be of ITO, or polycrystalline or amorphous silicon with appropriate conductivity. For actively address displays, the polycrystalline or amorphous Si could host thin film transistors. The top electrode could be comprised of metal or ITO electrodes.

Figure 7 shows the cross-sectional schematic of an injection based EL device incorporating nanophosphors utilizing a variety of silicon-layer 31-on-insulator 30 substrates. Layer 31 is used to form pixelated electrodes similar to that of Fig. 2. A thin layer of *p*-type semiconductor 32 with gap wider than that of CNCs layer 33 is grown to inject holes into the cores of CNCs. A thin hole-blocking layer 34 with a gap wider than CNC semiconductor core is grown on top of these CNCs, followed by a metal electrode 35. Hole blocking can be achieved by thin layers of semiconductors or insulators such as Ta₂O₅, Zn_xMg_{1-x}S, Zn_xBe_{1-x}S, etc. or their combination.

Figure 8 shows a hybrid (organic/inorganic) injection EL device. The *p*-type wider energy semiconductor layer 44 comprises of a hole-transporting (undoped) organic layer where its ionization potential is high enough to inject holes to CNCs (e.g. carbazole based derivatives, such as polyvinylcarbazole (PVK) and 4,4''-N,N'-dicarbazole-biphenyl (CBP) which can be spun-coated or vacuum deposited, respectively).

In Fig. 8(a), the CNCs (layer 43) are spun coated on top of *n*-type wide-energy inorganic semiconductor layer 42 (e.g. Zn_xMg_{1-x}S, Zn_xBe_{1-x}S) which is grown epitaxially on a thin *n*+ silicon layer 41 supported on a compatible substrate 40 (e.g. Si, sapphire). A thin layer of the hole-transporting organic semiconductor 44 is then grown on the CNC layer 43, followed by a conductive organic layer 45, which facilitates formation of ohmic contact with the top electrode 46.

In Fig. 8(b), layer 47 comprises of the hole-transporting organic semiconductor which is lightly doped with an oxidative agent such as (Fe^{III}citrate or Fe^{III}oxalate). The oxidation agent is selected such as not to dissociate away from its counter ion. For example, the Fe^{III}citrate is stabilized by multiple chelation. Building controllable thickness organic shells around the oxidation agent avoids permanent adsorption of oxidizing agent at the surface of CNCs. If this adsorption takes place (e.g. using FeCl₃) then the photoluminescence of CNCs is quenched. Therefore a thin shield is constructed

around the oxidizing agent or CNC utilizing appropriate counter ions, chelating agents, surfactants and dendrimers. When such a carefully-designed oxidizing agent is introduced in the vicinity of CNCs, it controllably extracts one electron from the CNC, leaving a hole which can recombine with an injected electron to produce a photon.

In Fig. 8(c), the hole-transporting organic acts as host to forms the matrix for CNCs (layer 48). Such composites can be easily spin coated.

Fig. 8(d) shows a structure similar to Fig. 8(c) where the composite of CNCs and the organic hole-transporting layer is doped with oxidative agent(s) discussed above, forming layer 49.

The profound permeability of organic compounds to moisture and oxygen requires the grown of a protective outer passivation layer on the CNCs. This is shown in Fig. 9, where the CNC, comprising of the core (50) and the pseudomorphic cladding (51), is coated with a passivation layer 52, (e.g. ZnO , SiO_x , SiON , Ta_2O_5 , etc.).

Fig. 10(a) shows an all-organic CNC-based injection EL device. A substrate (60) supports an ITO layer 61. With the help of an organic conductive layer 62, holes are easily introduced to a composite layer 63, comprised of CNCs and an organic hole-transporting agent. Subsequently, an electron transporting organic layer 64 is deposited. This is followed deposition of a thin (8-20 Å) tunneling layer 65, which is contacted with a top electrode (66). The organic conductive layer 62 may be realized by polyaniline or polythiophene based conductive polymers. The electron transporting layer 64 may be realized by bathocuproin, BCP, (a material with a 3.5 eV band gap, capable of injecting electrons in CNCs and blocking hole transport to the cathode). The electron-transporting layer could be combination of two layers having two different energy gaps such as CBP and aluminum (III) quinoline (Alq_3). The thin tunneling layer 65, may be composed from materials such as CsF , LiF , Al_2O_3 , etc. The top electrode 66, may be realized by metals (e.g. Al) or a thin-transparent metal layer covered with ITO.

Fig. 10(b) shows a structure similar to Fig. 10(a). Here, the layer 67, consisting of CNC and organic hole-transporting agent is doped with oxidative agent(s) described above.

Fig. 10(c) shows a structure similar to Fig. 10(a) where the CNC layer 68, is sandwiched between the hole-transporting layer 69 and electron transporting layer 64.

Fig. 10(d) shows a structure similar to Fig. 10(c), where the hole-transporting layer 70, is doped with an oxidative agent.

Fig. 11(a) shows an EL structure where a viscous, mellifluous composite layer 74, is sandwiched between two electrodes (72) supported on their respective substrate (71). The thickness of layer 74 is determined by the spacers (73). The viscous composite may consists of CNCs, hole-transporting organic semiconductors, oxidative agents, soluble salts (e.g. lithium triflate, lithium acetate, etc.) and low vapor pressure viscosity-modifying agents (e.g. dioctyl phthalate, low molecular weight polyethylene oxide, etc.). The spacers (73) may comprise of monodispersed glass microspheres.

Fig. 11(b) shows an EL structure similar to that on Fig. 11(a). Here the 3-dimensional view of a display employing passively-matrix-address scheme is illustrated. Rows and columns of the bottom and top electrodes respectively are separated by an elastomeric spacer 75 (e.g. polyurethane, polysiloxane elastomer).

With a help of a microlithographically defined 3-D mold, monomeric or oligomeric precursors are squeezed and polymerized in place between the bottom substrate and the mold. Subsequently, the mold is removed and the viscous composite (74) comprising of CNCs, hole-transporting organic semiconductors, oxidative agents, soluble salts and low vapor pressure viscosity-modifying agents is either screen-printed or ink jet printed to fill the cavities within the elastomeric spacer (75). Red, green and blue RGB CNCs are used to define the three colors in the viscous composite (74R, 74G, 74B) to realize full color pixelated displays. The final step seals the structure by placing the cover (top substrate (77) and contacts (76)). Such structures are expected to be mass-produced adapting techniques known in the prior art.

Fig. 12 shows an EL structure employing nanotip Si emitters (realized in the base region using the selective area epitaxy through thin SiO_2 masks) providing minority carriers into the nanophosphor layer (85). The p-Si base layer (81 and 83) and nanophosphor layer along with n-type wide energy gap outer layer (86) forming the p-n junction are reversed biased. Collected electrons [which are injected from the n-layer (81) underneath the pSi layer 81] are introduced in the cores of nanocrystals where they recombine with holes (supplied by the top contact electrode (87)). A typical fabrication methodology to realize this structure is described below.

quantum dots with coupled cores are all within the scope of this invention. We have described p-n structures sandwiching quantum dot layer(s) for injection or avalanche type EL displays, Schottky barrier contacts can also be used. Wider energy gap semiconductor layers, dielectric layers, as well as quantum dots can be selected and patterned (in order to facilitate various addressing schemes, such as passive and active matrix) from a wide ranging material list. In addition, electrodes can be isolated via oxide regions (as shown in figures), reverse biased junctions, and other standard methods.

Although we have described EL devices, the structure is adaptable to cathodoluminescence devices which are addressed by electron beam replacing the regular phosphor by quantum dot based nanophosphor. Various organic structures showing hole transport and electron transport layers can also be realized with variations in materials selected. We have not shown active matrix addressing. In Si on Insulator substrates, one can implement transistors in the top Si layer. Other logic devices can be integrated if needed.

CLAIMS

1. A *p-n* junction electroluminescent (EL) device, comprising multiple layers of:
 - a semiconductor-on-insulator substrate;
 - a first *p*-doped Si layer grown on the said substrate,
 - part of the layer being oxidized to isolate bottom electrodes of said device;
 - a thin-layer of Si which allows further epitaxial growth;
 - a *p*-doped wide energy gap semiconductor layer grown epitaxially;
 - a layer comprising pseudomorphic cladded quantum dots nanocrystals (CNCs) deposited on the said wide energy gap layer;
 - a thin wide energy gap semiconductor layer having n-type conductivity, grown on the CNC layer; and
 - a metal layer forming a plurality of top contact electrodes deposited on the wide energy gap semiconductor layer having appropriate patterned regions to confine current conduction in desired pixels of said EL device.
2. A Schottky barrier electroluminescent device, comprising multiple layers on a semiconductor on insulator substrate having:
 - a first *n*-doped Si layer grown on the said substrate, having a plurality of rows of oxide isolation separating bottom electrodes of said EL device;
 - a thin-layer of Si which allows further epitaxial growth;
 - a *n*-doped wide energy gap semiconductor layer grown epitaxially;
 - a CNC layer of pseudomorphic cladded quantum dots nanocrystals (CNCs) deposited on said wide energy gap layer;
 - a thin wide energy gap semiconductor layer grown on the CNC layer; and
 - a metal layer deposited selectively on the wide energy gap semiconductor layer to form a top Schottky contact electrodes.
3. An EL device of claim 1, wherein said CNC layer are selected from the group of semiconductor materials consisting of $\text{Zn}_x\text{Cd}_{1-x}\text{Se}$ (core) - $\text{Zn}_y\text{Mg}_{1-y}\text{Se}$ (cladding), $\text{Zn}_x\text{Cd}_{1-x}\text{Se}$ (core) - $\text{Zn}_z\text{Be}_{1-z}\text{Se}$ (cladding), $\text{Zn}_x\text{Cd}_{1-x}\text{Se}$ (core) - ZnMgSSe (cladding),

In_xGa_{1-x}N (core) - GaN (cladding), GaN (core)-AlGaN (cladding), and ZnCdS (core)-ZnMgS (cladding).

4. An EL device of claim 2, wherein said CNC layer are selected from the group of semiconductor materials consisting of Zn_xCd_{1-x}Se (core) - Zn_yMg_{1-y}Se (cladding), Zn_xCd_{1-x}Se (core) - Zn_zBe_{1-z}Se (cladding), Zn_xCd_{1-x}Se (core) - ZnMgSSe (cladding), In_xGa_{1-x}N (core) - GaN (cladding), GaN (core)-AlGaN (cladding), and ZnCdS (core)-ZnMgS (cladding).

5. An EL device of claim 3, wherein said CNC layer is sandwiched between compatible wide energy gap semiconductor layers selected from the group of semiconductors consisting of Zn_aMg_{1-a}Se, Zn_aMg_{1-a}S, Zn_aMg_{1-a}SbSe_{1-b}, Zn_aBe_{1-a}SbSe_{1-b}, Al_cGa_{1-c}N, and AlInN.

6. An EL device of claim 1, wherein said *p-n* junction is reverse-biased to operate said device in the avalanche mode.

7. An EL device of claim 1, wherein said *p-n* junction is forward-biased to operate in injection mode.

8. An EL device of claim 1, wherein the CNC layer comprises multiple layers of CNCs sandwiched between epitaxially grown thin film layers of wide energy gap semiconductors.

9. An EL device of claim 2, wherein the CNC layer comprises multiple layers of CNCs sandwiched between epitaxially grown thin film layers of wide energy gap semiconductors.

10. An EL device as described in claim 1, wherein said CNC layer has more than one sublayers stacked to emit different colors and white-light

11. An EL device as described in claim 2, wherein said CNC layer has more than one sublayers stacked to emit different colors and white light
12. An EL device as described in claim 1, wherein more than one said CNC layer are deposited to produce red, green and blue pixel elements for a display panel.
13. An EL device as described in claim 2, wherein more than one said CNC layer are deposited to produce red, green and blue pixel elements for a display panel.
14. An electroluminescent (EL) device, comprising multiple layers of:
- a plurality of transparent electrodes on an insulator substrate;
 - a first dielectric layer deposited on said set of transparent electrodes;
 - a CNC layer comprising pseudomorphic cladded quantum dots nanocrystals (CNCs) deposited on said dielectric layer;
 - a second dielectric layer deposited on said CNC layer; and
 - a metal layer forming top contact electrode is deposited on the second dielectric layer.
15. An electroluminescent device of claim 14, wherein the CNC layer is selected from the group of said CNC consisting of: $\text{Zn}_x\text{Cd}_{1-x}\text{Se}$ (core)- $\text{Zn}_y\text{Mg}_{1-y}\text{Se}$ (cladding), $\text{Zn}_x\text{Cd}_{1-x}\text{Se}$ (core)- $\text{Zn}_z\text{Be}_{1-z}\text{Se}$ (cladding), $\text{Zn}_x\text{Cd}_{1-x}\text{Se}$ (core)- ZnMgSSe (cladding), $\text{In}_x\text{Ga}_{1-x}\text{N}$ (core)- GaN (cladding), GaN (core)- AlGaN (cladding), and ZnCdS (core)- ZnMgS (cladding).
16. An electroluminescent device of claim 14, wherein the first dielectric layer and the second dielectric layer are selected from the group consisting of SiON , Ta_2O_5 , $\text{Ba}_x\text{Sr}_{1-x}\text{TiO}_3$, PLZT , $\text{Zn}_x\text{Mg}_{1-x}\text{S}$, $\text{Zn}_x\text{Be}_{1-x}\text{S}$, etc., and their combination.
17. An EL device of claim 14 wherein said EL device is operating in the avalanche mode using an AC bias.
18. An electroluminescent (EL) device, comprising multiple layers of:

a *p*-doped silicon-on-insulator (SOI) substrate;
 a *p*-doped wider energy gap semiconductor layer grown epitaxially on said *p*-doped SOI substrate;
 a CNC layer of pseudomorphic cladded quantum dots nanocrystals (CNCs) deposited on the insulator of said SOI substrate;
 a hole-blocking layer deposited on said CNC layer; and
 a metal layer forming top contact electrodes deposited on the hole-blocking layer.

19. An electroluminescent device of claim 18, wherein the CNC layers comprise of $\text{Zn}_x\text{Cd}_{1-x}\text{Se}$ (core)- $\text{Zn}_y\text{Mg}_{1-y}\text{Se}$ (cladding), $\text{Zn}_x\text{Cd}_{1-x}\text{Se}$ (core)- $\text{Zn}_z\text{Be}_{1-z}\text{Se}$ (cladding), $\text{Zn}_x\text{Cd}_{1-x}\text{Se}$ (core)- ZnMgSSe (cladding), $\text{In}_x\text{Ga}_{1-x}\text{N}$ (core)- GaN (cladding), GaN (core)- AlGaN (cladding), and ZnCdS (core)- ZnMgS (cladding).

20. An electroluminescent device of claim 18, wherein the hole-blocking layer is selected from the group consisting of Ta_2O_5 , $\text{Zn}_x\text{Mg}_{1-x}\text{S}$, $\text{Zn}_x\text{Be}_{1-x}\text{S}$, and ZnMgBeSe .

21. An EL device of claim 18, wherein said EL device operates in an injection mode using a forward bias across said device.

22. An electroluminescent (EL) device, comprising multiple layers of:

an *n*-doped silicon layer on insulator substrate with contact electrodes;
 a *n*-doped wider energy gap semiconductor layer grown epitaxially on said Si layer;
 a CNC layer of pseudomorphic cladded quantum dots nanocrystals (CNCs) deposited on said wider energy gap semiconductor layer;
 a hole-transporting layer of wide-energy gap organic semiconductor on said CNC layer;
 an organic conductive layer deposited on said hole-transporting layer; and
 a metal layer forming top contact electrodes deposited on the said organic conductive layer.

23. An electroluminescent device of claim 22, wherein more than one said CNC layer are selected from the group consisting of: $\text{Zn}_x\text{Cd}_{1-x}\text{Se}$ (core)- $\text{Zn}_y\text{Mg}_{1-y}\text{Se}$ (cladding), $\text{Zn}_x\text{Cd}_{1-x}\text{Se}$ (core)- $\text{Zn}_z\text{Be}_{1-z}\text{Se}$ (cladding), $\text{Zn}_x\text{Cd}_{1-x}\text{Se}$ (core)- ZnMgSSe (cladding), $\text{In}_x\text{Ga}_{1-x}\text{N}$ (core)- GaN (cladding), GaN (core)- AlGaN (cladding), and ZnCdS (core)- ZnMgS (cladding).

24. An electroluminescent device of claim 22, wherein the *n*-type wide-energy gap inorganic semiconductor layer is selected from the group consisting of semiconductors $\text{Zn}_a\text{Mg}_{1-a}\text{Se}$, $\text{Zn}_a\text{Mg}_{1-a}\text{S}$, $\text{Zn}_a\text{Mg}_{1-a}\text{SbS}_{1-b}$, $\text{Zn}_a\text{Be}_{1-a}\text{SbS}_{1-b}$, $\text{Al}_c\text{Ga}_{1-c}\text{N}$, ZnMgBeSe , and AlInN .

25. An electroluminescent device of claim 22, wherein the hole-transporting layer is selected from the group consisting of PVK and CBP.

26. An electroluminescent device of claim 22, wherein the hole-transporting layer is doped with an oxidative agent selected from the group of compounds such as Fe^{III} citrate and Fe^{III} oxalate.

27. An EL device as described in claim 26, wherein the oxidative agent is constructed with a thin shield around the oxidizing agent utilizing appropriate counter ions, chelating agents, surfactants and dendrimers.

28. An EL device as described in claim 23, wherein the CNC nanocrystals are constructed with a thin shield around the outer core utilizing appropriate counter ions, chelating agents, surfactants and dendrimers.

29. An electroluminescent device of claim 22, wherein the CNC layer is merged with the hole-transporting layer.

30. An electroluminescent device of claim 29, wherein the composite of CNC nanocrystals and hole-transporting layer is doped with oxidative agent, selected from a

group of compounds such as Fe^{III} citrate and Fe^{III} oxalate; the said oxidative agents are constructed with a thin shield around them utilizing appropriate counter ions, chelating agent, surfactants and dendrimers.

31. An electroluminescent device of claim 29, wherein the composite of CNC nanocrystals and hole-transporting layer is doped with oxidative agent selected from the group of compounds consisting of Fe^{III} citrate or Fe^{III} oxalate; the said CNCs are constructed with a thin shield around them utilizing appropriate counter ions, chelating agent, surfactants and dendrimers.

32. An EL device as described in claim 1 wherein said CNC layer is coated with an environmental passivation layer selected from the list of compounds consisting of ZnO , SiO_x , SiON , and Ta_2O_5 .

33. An electroluminescent (EL) device, comprising multiple layers on an insulator substrate of:

- a transparent electrode;;
- an organic conductive layer deposited on said transparent electrode;
- a composite layer, comprising CNC nanocrystals and an organic hole-transporting agent on said organic conductive layer;
- an electron transporting organic layer on said composite layer;
- a thin (8-20 Å) tunneling layer deposited on electron transporting layer; and
- a metal layer forming top contact electrode deposited on the said tunneling layer.

34. An electroluminescent device of claim 33 wherein the composite layer and hole-transporting layer is doped with oxidizing agent selected from the group of compounds consisting of Fe^{III} citrate and Fe^{III} oxalate; and

said oxidative agents is constructed with a thin shield around said CNC nanocrystals utilizing appropriate counter ions, chelating agent, surfactants and dendrimers.

35. An electroluminescent device of claim 33 wherein the composite of CNC nanocrystals and hole-transporting layer is doped with oxidative agent selected from the group of compounds consisting of Fe^{III} citrate and Fe^{III} oxalate; and

the said CNC nanocrystals are constructed with a thin shield around them utilizing appropriate counter ions, chelating agent, surfactants and dendrimers.

36. An EL device of claim 33, wherein the composite of CNCs and hole-transporting layer are separated in individual layers.

37. An electroluminescent device of claim 36, wherein the hole transporting layer is doped with an oxidative agent selected from the group of compounds consisting of Fe^{III} citrate and Fe^{III} oxalate; and

the said oxidative agents are constructed with a thin shield around them utilizing appropriate counter ions, chelating agent, surfactants and dendrimers.

38. An electroluminescent device, comprising multiple layers on an insulator substrate of:
a transparent electrode;

a viscous composite, comprising CNCs, hole-transporting organic semiconductors, oxidative agents, soluble salts and low vapor pressure viscosity-modifying agents;

wherein said viscous composite is sandwiched between the said transparent electrode and

a second electrode, which is separated by uniform spacers.

39. An electroluminescent device of claim 38, wherein said spacers are made of elastomers containing appropriate holes for containing said viscous composite.

40. An electroluminescent device of claim 39, wherein the holes in the said elastomeric spacers are filled with said viscous composites with distinct light emission characteristics.

41. An EL device as described in claim 40, wherein the viscous composite is introduced by a method selected from the group consisting of screen-printing and ink-jet printing.

42. An electroluminescent (EL) device, comprising multiple layers, on an insulator substrate, of:

a *n*-doped silicon layer, comprising thin doped Si *n/n+* regions separated by insulating regions, such as SiO₂, and contacted to form bottom electrodes;

a thin-layer of Si which allows further epitaxial growth;

a *p*-type Si layer, having addressing contact electrodes;

a thin (about 10 nm) SiO₂ layer deposited and patterned with a pitch of about 0.1 microns;

a *p*-Si layer forming nanotips;

an *n*-type wide energy gap layer selected from the group of semiconductors consisting of Zn_aMg_{1-a}Se, Zn_aMg_{1-a}S, Zn_aMg_{1-a}S_bS_{1-b}, Zn_aBe_{1-a}S_bS_{1-b}, Al_cGa_{1-c}N, ZnMgBeSe, and AlInN stacked on the *p*-Si layer layer with nanotips;

a layer comprising cladded quantum dots;

a wide gap semiconductors layer selected from the group of semiconductors consisting of: Zn_aMg_{1-a}Se, Zn_aMg_{1-a}S, Zn_aMg_{1-a}S_bS_{1-b}, Zn_aBe_{1-a}S_bS_{1-b}, Al_cGa_{1-c}N, ZnMgBeSe, and AlInN;

a layer forming contact electrodes;

said set of electrodes being appropriately biased and addressed to create a two-dimensional display.

43. An EL device as described in claim 42 wherein the layers starting from the substrate are of *p-n-p* configuration; said bottom electrodes are *p/p+* type, the middle layer having nanotips is *n*-type, and the wide energy gap layers sandwiching the nanoparticles are *p*-type semiconductors.

44. An EL device as described in claim 1 where the bottom electrodes are separated by technique other than oxidation such as reverse biased junctions.

ABSTRACT OF THE DISCLOSURE

This invention discloses novel device structures for full color flat panel displays utilizing pseudomorphically cladded quantum dot nanocrystals. Different colors are obtained by changing the core size and composition of the quantum dots while maintaining a nearly defect-free lattice at the core-cladding interface. Light emission from the quantum dot core is obtained either by injection or by avalanche electroluminescence. A nanotip emitter device is also presented. These generic devices can be addressed using a variety of conventional display drivers, including active and passive matrix configurations.

REFERENCES:

1. A. P. Alivisatos, *J. Phys. Chem.*, **100**, 13226, (1996).
2. Xiaogang Peng, Michael C. Schlamp, Andreas V. Kadavanich, and A. P. Alivisatos, *Journal of the American Chemical Society*, **1997**, *119*(30); 7019-7029.
3. M. A. Hines, J. Guyot-Sionnest, *J. Phys. Chem.*, **100**, 468 (1996).
4. B. O. Dabbousi, J. Rodriguez-Viejo, F. V. Mikulec, J. R. Heine, H. Mattoussi, R. Ober, K. F. Jensen, and M. G. Bawendi, *J. Phys. Chem. B*, **101**, 9463-9475, 1997.
5. M. Nirmal, B. O. Dabbousi, M. G. Bawendi, J. J. Macklin, J. K. Trautman, T. D. Harris, and L. E. Brus, *Nature*, **383**, 802, (1996).
6. F. Jain and W. Huang, Modeling of Transitions in Mn^{2+} Doped ZnS Nanocrystals and Predicting Reduced Threshold Current Density and Enhanced Electrooptic Effects in ZnCdSe-ZnMgSSe and InGaN-AlGaIn Pseudomorphic Quantum Dots, *J. Appl. Phys.*, **85**, pp. 2706-2712, March 1999.
7. S. Shoji, K. Mukai, N. Ohtsuka, M. Sugawara, T. Uchida, and H. Ishikawa, Lasing at three-dimensionally quantum-confined sublevel of self-organized $In_{0.5}Ga_{0.5}As$ quantum dots by current injection, *IEEE Photonics Tech. Lett.*, **7**, pp. 1385-1387, December 1995.
8. Hedi Mattoussi, Leonard H. Radzilowski, Bashir O. Dabbousi, Edwin L. Thomas, Mouni G. Bawendi, and Michael F. Rubner, *J. Appl. Phys.*, **83**, 7965-7974 (1998)
9. R. N. Bhargava, Gallagher, D.; Hong, X.; Nurmikko, A. *Phys. Rev. Lett.* **1994**, *72*, 416; R.N. Bhargava, Gallagher, D., and Welker, T., *J. Lum.* **1994**, *60*, 5278; D. Gallagher, Bhargava, R. N.; Healy, W. E.; Bhargava, R. N. *J. Mater. Res.* **1995**, *10*, 870.
10. F. Jain, S. Srinivasan, E. Heller, R. LaComb, M. Gokhale, and W. Huang, Monolithic Integration of Red, Blue, and Green Lasers for Smart Projection Displays, *J. Soc. Information Displays*, Vol.5, No.3, pp. 241-250, November 1997.
11. S. Tsukamoto, Y. Nagamune, M. Nishioka, and Y. Arakawa, Fabrication of GaAs Quantum Wires (~10nm) by Metalorganic Chemical Vapor Selective Deposition Growth, *Appl. Phys. Lett.* **63**, pp. 355-357, 1993.

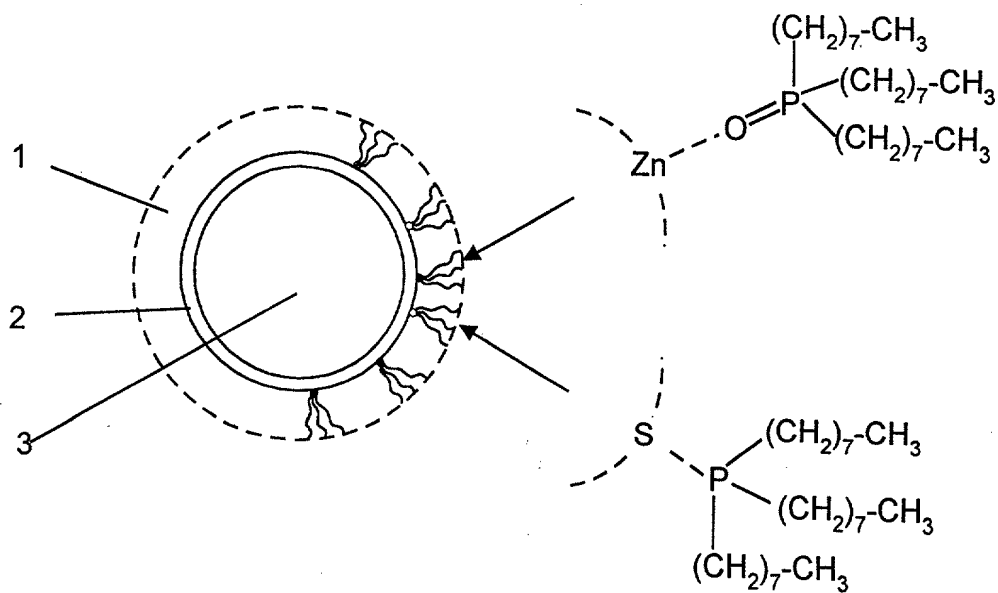


Fig. 1a Prior Art

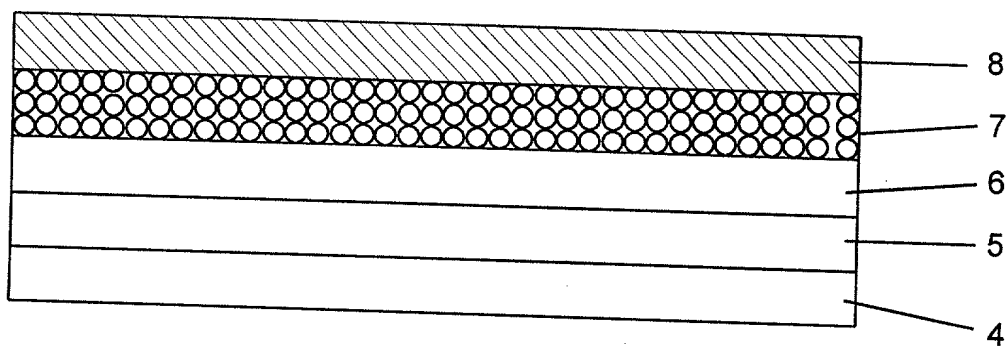


Fig. 1b Prior Art

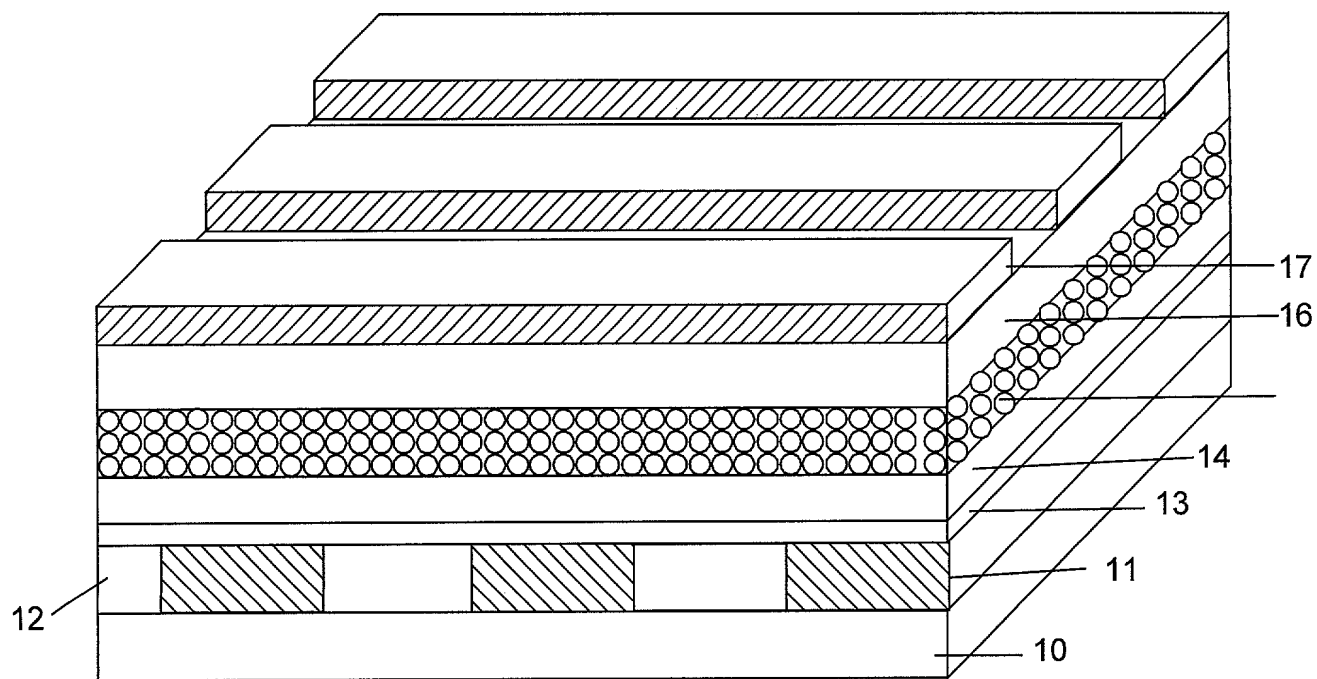


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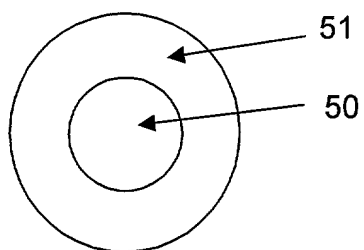


Fig. 2b

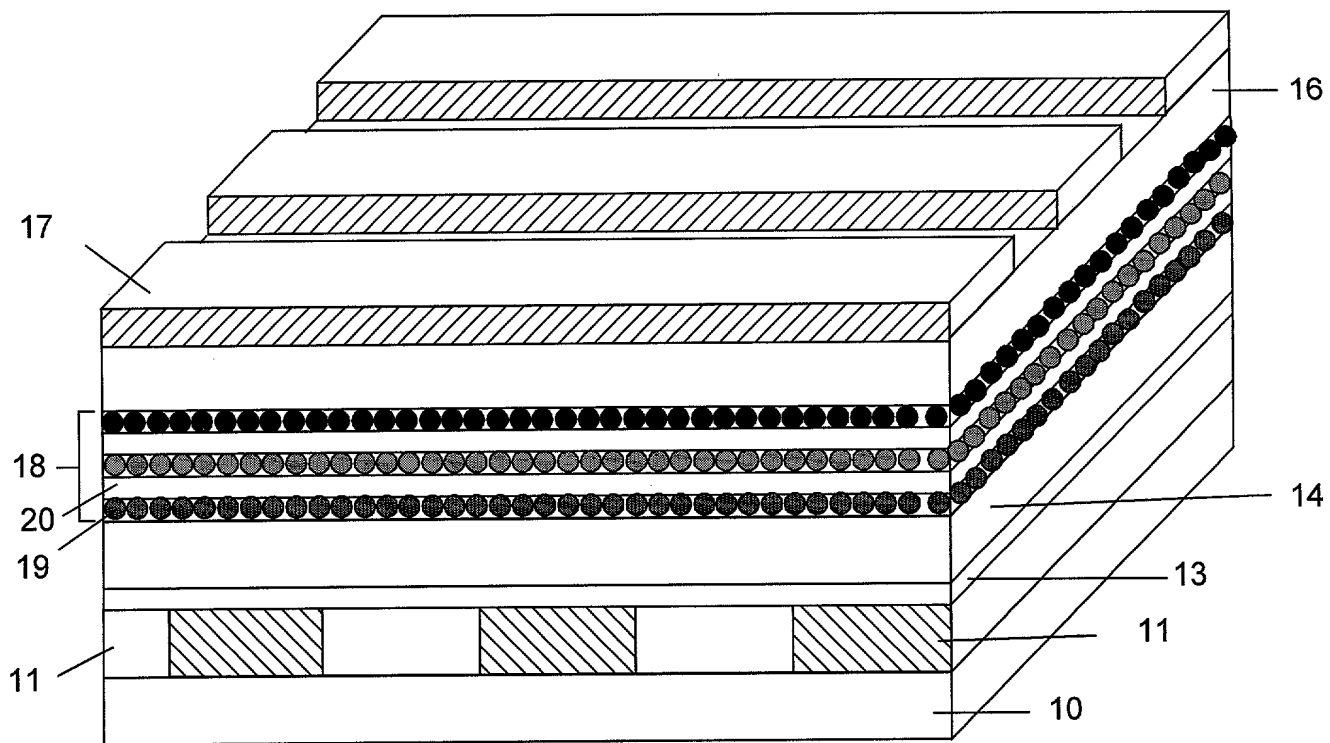
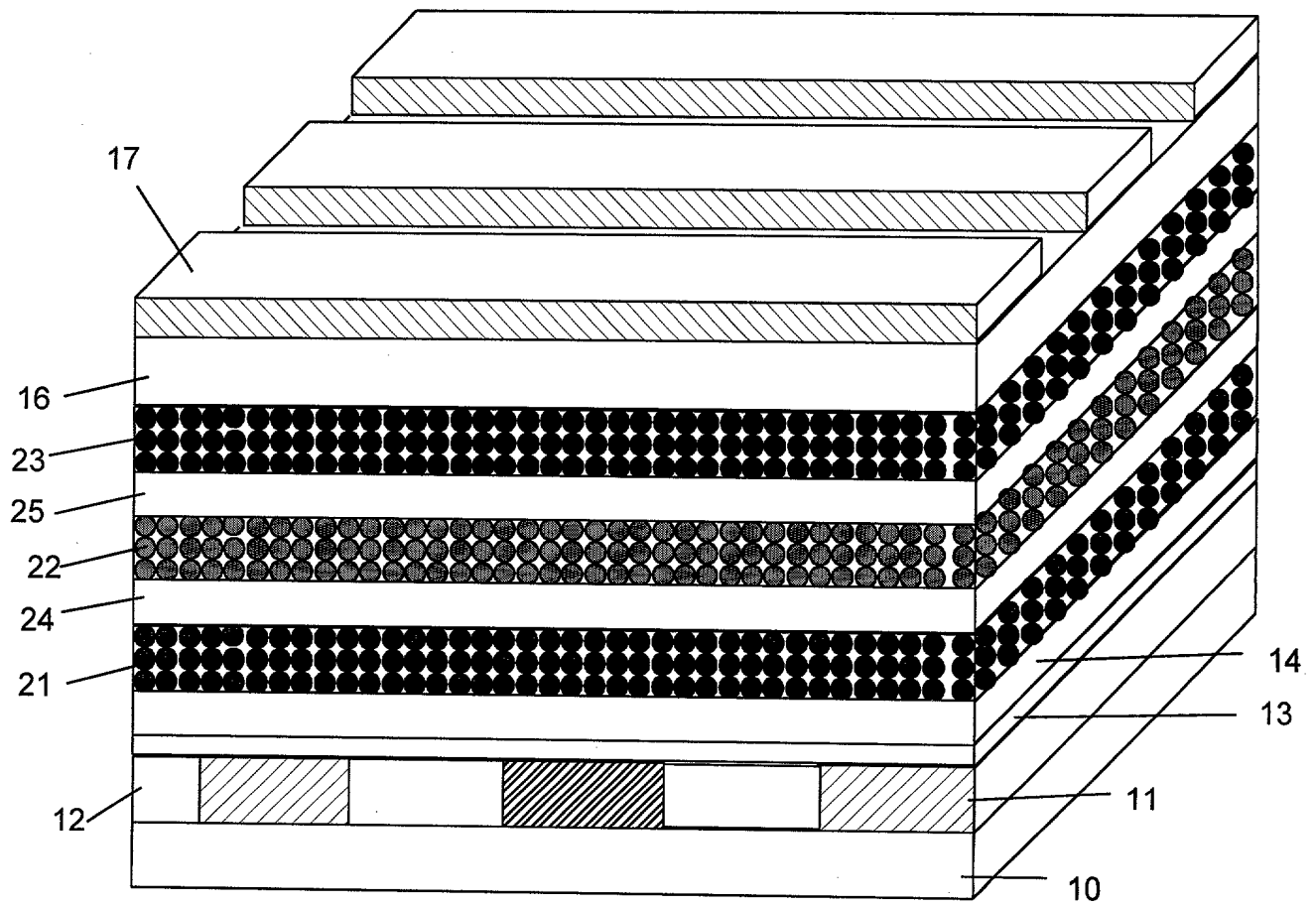


Fig. 3

Fig. 4



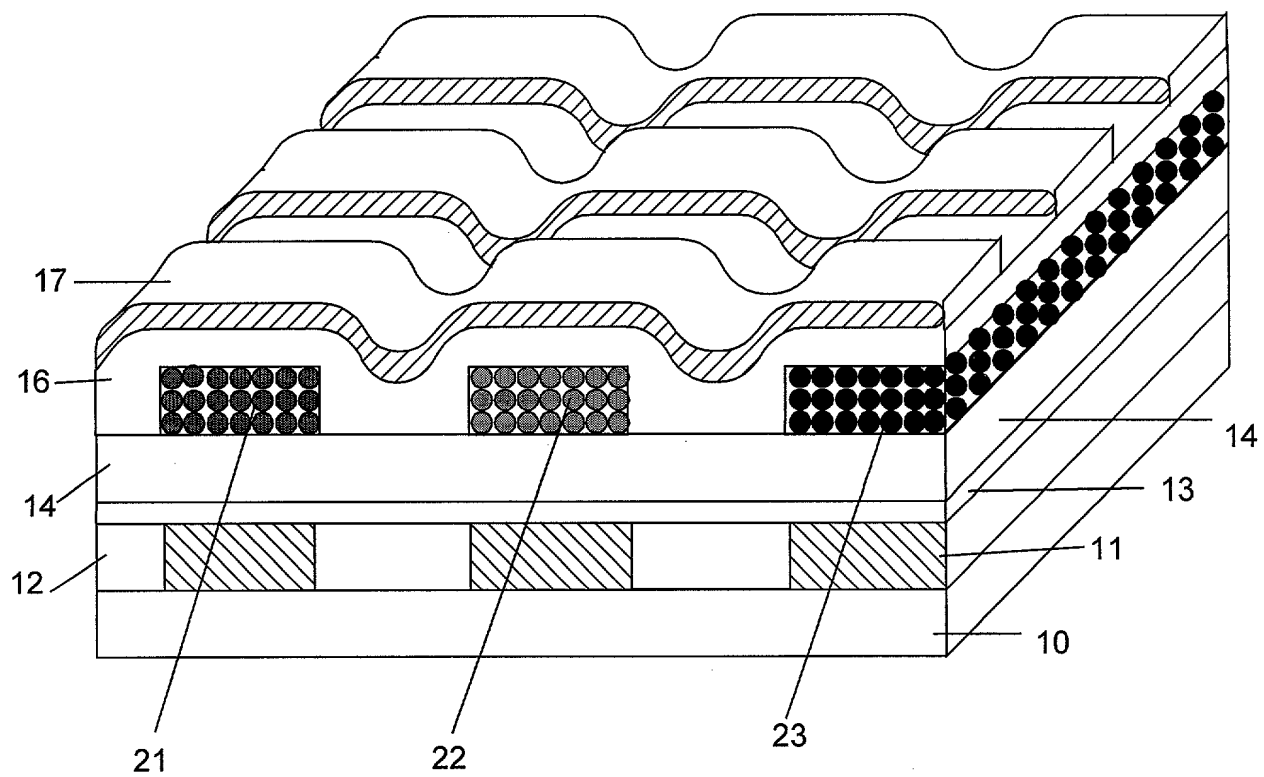


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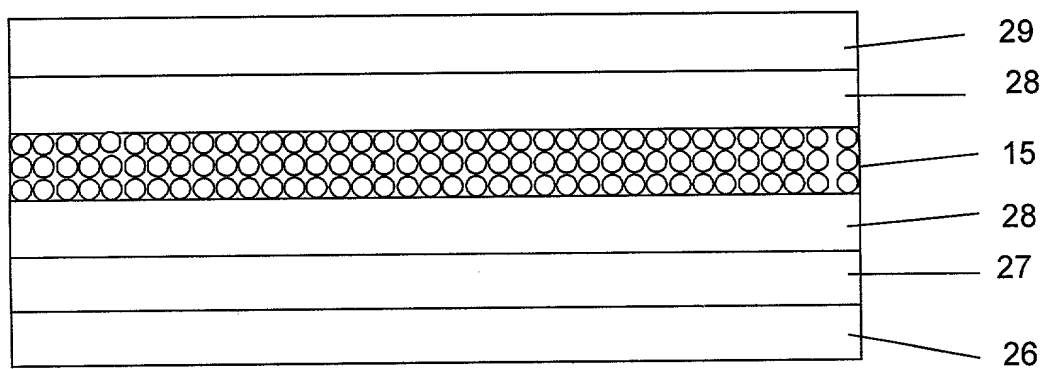


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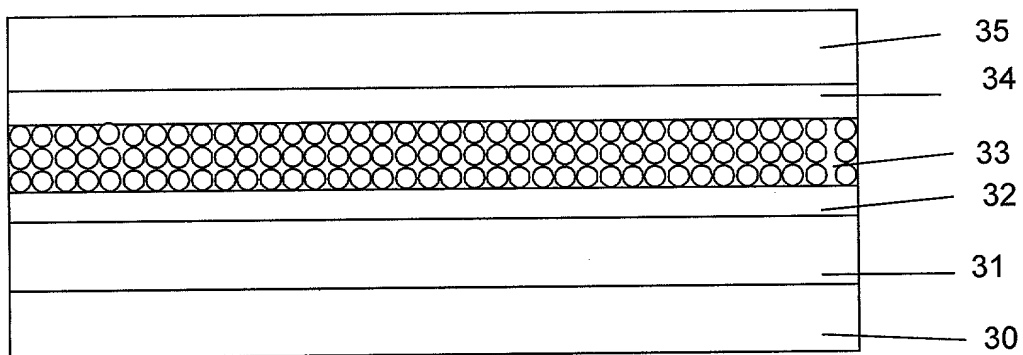


Fig. 7

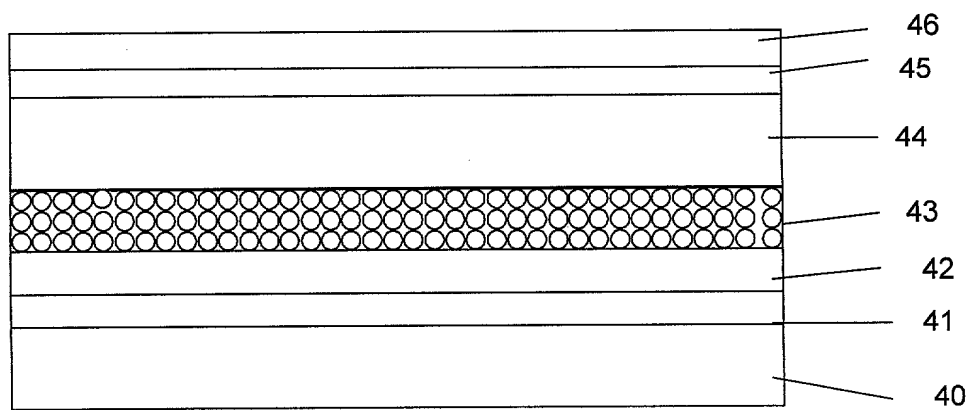


Fig. 8(a)

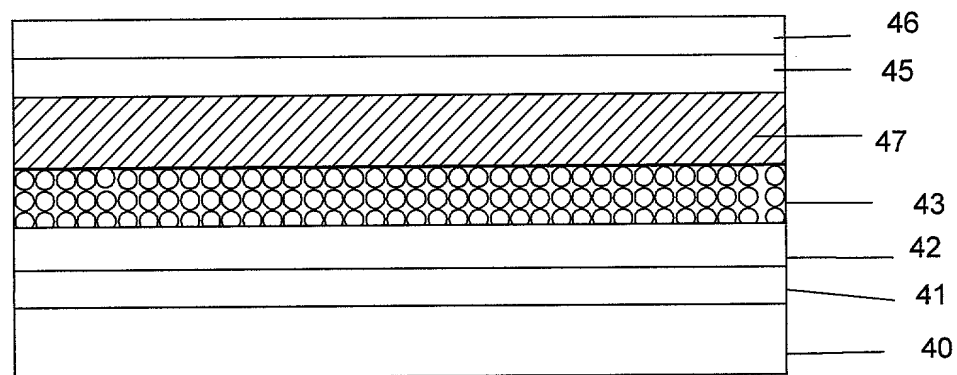


Fig. 8(b)

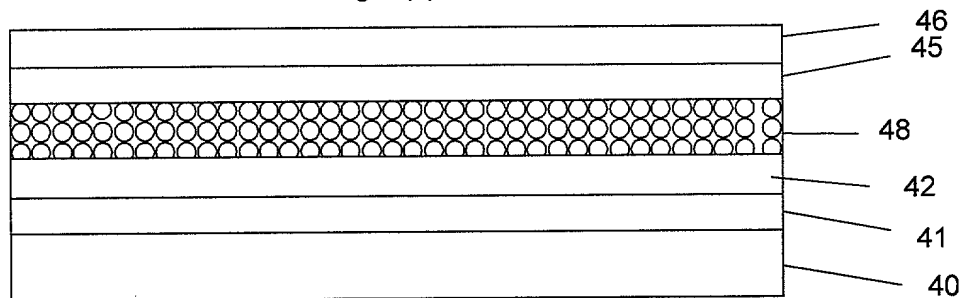


Fig. 8(c)

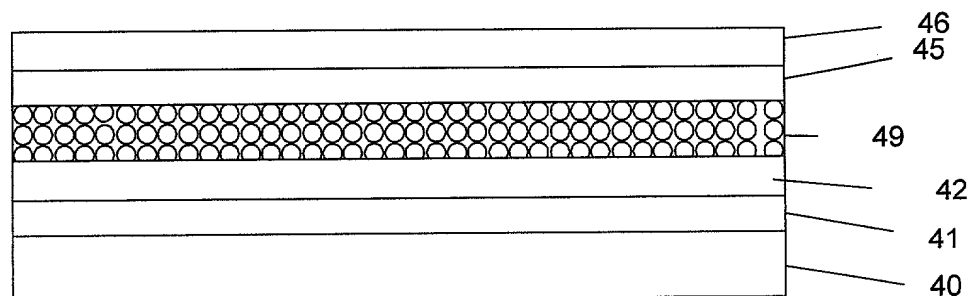


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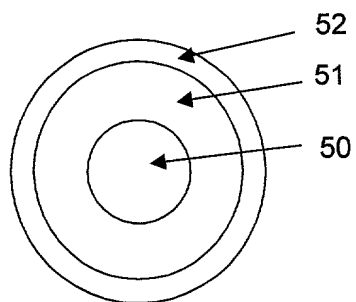


Fig. 9

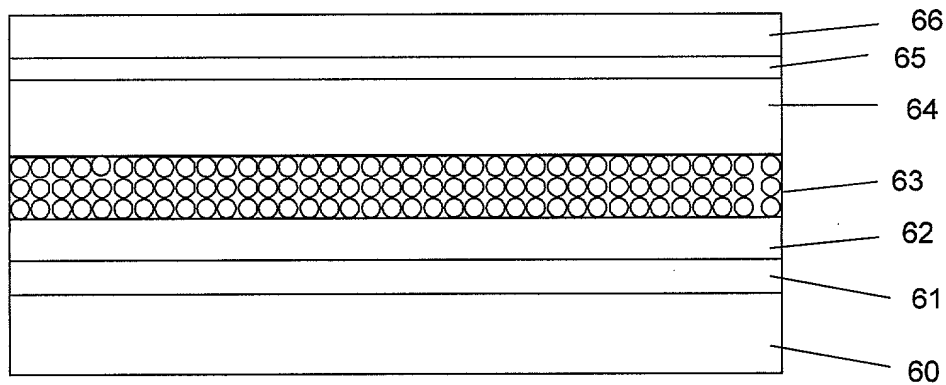


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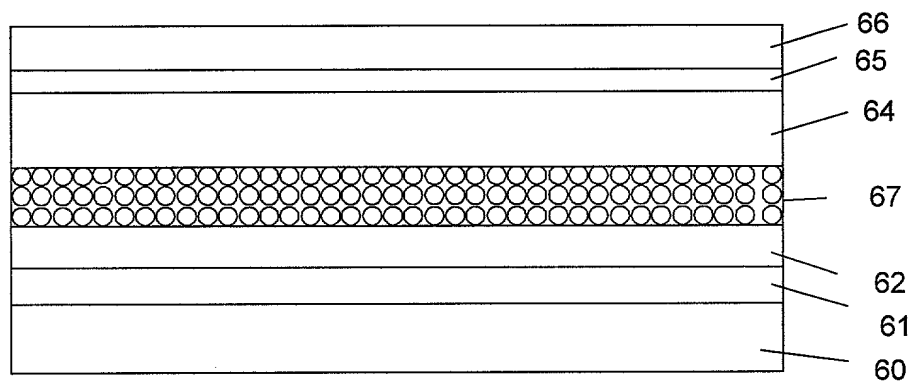


Fig. 10(b)

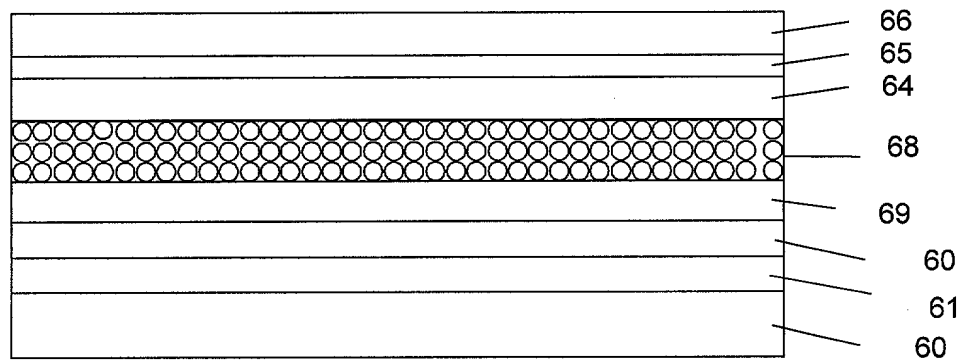


Fig. 10(c)

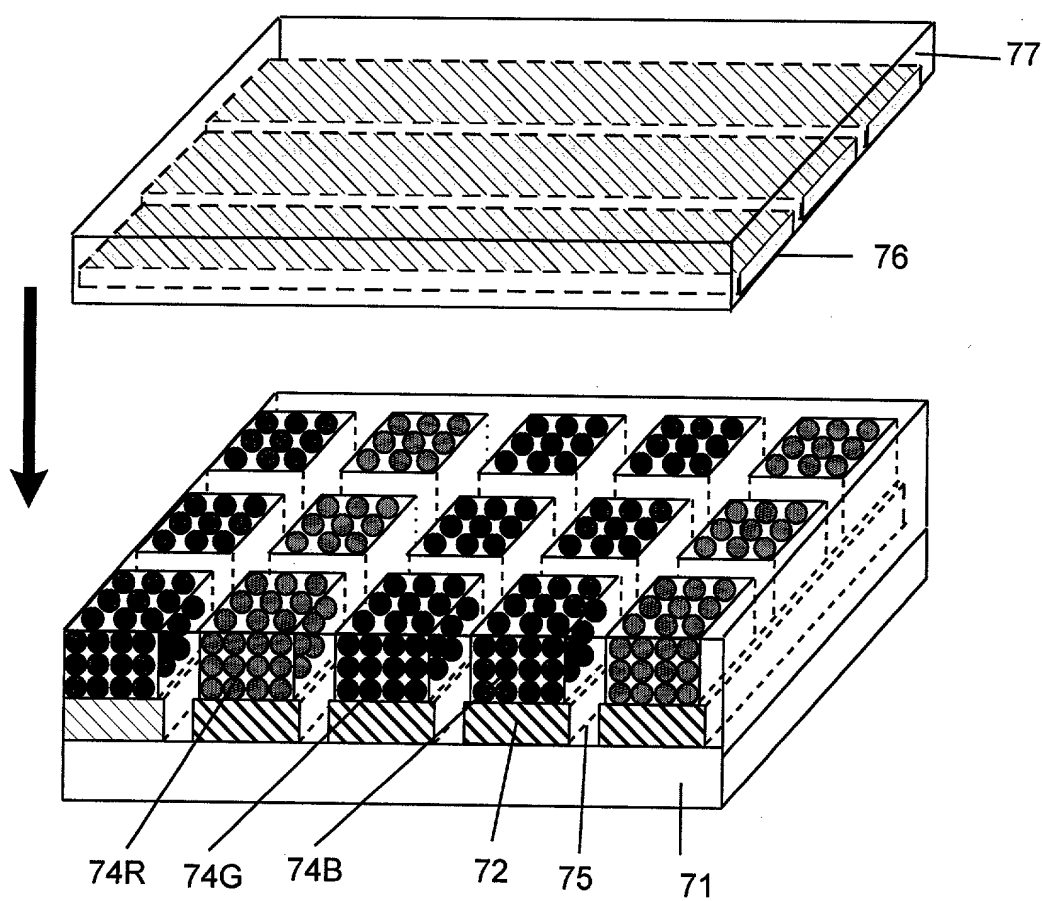
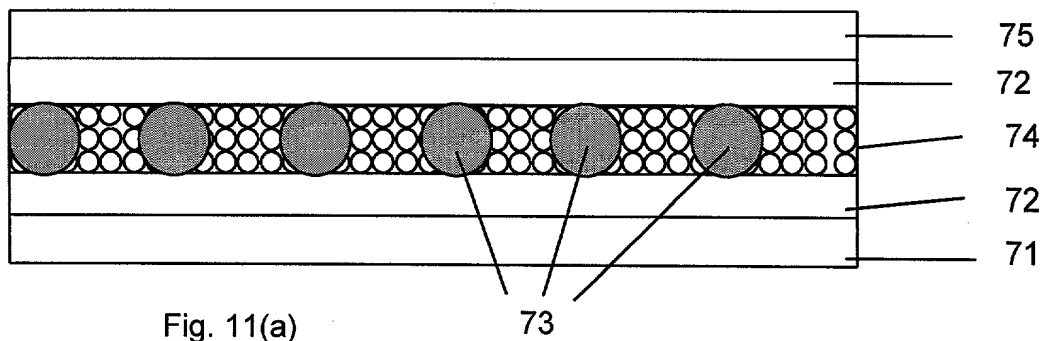


Fig. 12.

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**DECLARATION FOR UTILITY OR
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PATENT APPLICATION
(37 CFR 1.63)**

☒ Declaration
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Filing OR ☐ Declaration
Submitted after Initial
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(37 CFR 1.16 (e))
required)

Attorney Docket Number	Conn-2
First Named Inventor	Faquir C. JAIN
COMPLETE IF KNOWN	
Application Number	/
Filing Date	
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As a below named inventor, I hereby declare that:

My residence, post office address, and citizenship are as stated below next to my name.

I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled:

Full Color Display Structures using Pseudomorphic Cladded Quantum Dot...Thin Films

the specification of which
☒ is attached hereto
OR
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Application Number and was amended on (MM/DD/YYYY) (if applicable).

I hereby state that I have reviewed and understand the contents of the above identified specification, including the claims, as amended by any amendment specifically referred to above.

I acknowledge the duty to disclose information which is material to patentability as defined in 37 CFR 1.56.

I hereby claim foreign priority benefits under 35 U.S.C. 119(a)-(d) or 365(b) of any foreign application(s) for patent or inventor's certificate, or 365(a) of any PCT international application which designated at least one country other than the United States of America, listed below and have also identified below, by checking the box, any foreign application for patent or inventor's certificate, or of any PCT international application having a filing date before that of the application on which priority is claimed.

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[Page 1 of 2]

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U.S. Parent Application or PCT Parent Number	Parent Filing Date (MM/DD/YYYY)	Parent Patent Number (if applicable)

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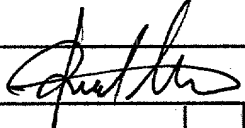
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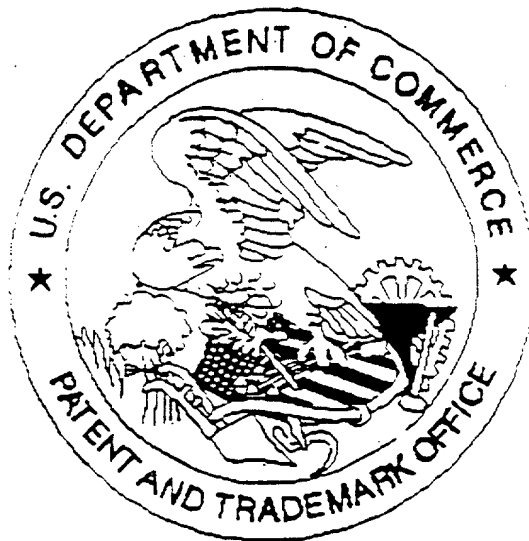
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